

Electronic Acknowledgement Receipt

EFS ID:	7049677
Application Number:	10829495
International Application Number:	
Confirmation Number:	6926
Title of Invention:	Method and system for fail-safe renaming of logical circuit identifiers for rerouted logical circuits in a data network
First Named Inventor/Applicant Name:	William Taylor
Customer Number:	83417
Filer:	Felipe Hernandez/Kathryn Bartelt
Filer Authorized By:	Felipe Hernandez
Attorney Docket Number:	20103/030290
Receipt Date:	19-FEB-2010
Filing Date:	22-APR-2004
Time Stamp:	17:06:24
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Electronic Funds Transfer
Payment was successfully received in RAM	\$180
RAM confirmation Number	3858
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
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1	Transmittal Letter	030290_IDS_Transmittal_21910.pdf	63188 b7c21bb69a6c918eeef5610ab4e83 ca98	no	1
Warnings:					
Information:					
2	Information Disclosure Statement (IDS) Filed (SB/08)	030290_IDS_21910.pdf	37430 1263c2011296ab6c770ebea4ffab53d5e0 24f5	no	4
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
3	Fee Worksheet (PTO-875)	fee-info.pdf	30282 6abac74d454debd08b714-82356261E50 860109	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				130900	
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